

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shunpei YAMAZAKI et al.
Serial No. 09/535,015
Filed: March 24, 2000
For: METHOD OF MANUFACTURING A
SEMICONDUCTOR DEVICE



) Group Art Unit: 2811

) Examiner: S. Crane

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is
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) Commissioner for Patents, P.O. Box 1450,
) Alexandria, VA 22313-1450, on June 1, 2006.

Adrian M. Stamps

**INFORMATION DISCLOSURE STATEMENT AND CORRECTION OF PREVIOUSLY
SUBMITTED INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

This Information Disclosure Statement is being submitted with an RCE. Therefore, no fee is required.


Further to the Correction to Previously Submitted Information Disclosure Statement filed April 28, 2006, it has come to applicant's attention that the Correction included the following inaccuracy:

1) The article to Nemanich et al., *Structure and Growth of the Interface of Pd on a-SiH*, cited the date as June 1981. The complete date is June 15, 1981.

The above inaccuracy is corrected on the 1449 Form attached hereto. The Examiner is requested to initial the attached PTO Form 1449 evidencing consideration of the above reference and line through this reference on the previously submitted

Information Disclosure Statements to ensure that the correct information is printed correctly on any issued patent.

Respectfully submitted,



Eric J. Robinson
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789

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